

2655

<b>AMENDMENT TRANSMITTAL LETTER</b>			Docket No. SON-2274
Application No. 10/000,347-Conf. #9724	Filing Date December 4, 2001	Examiner Gautam Patel	Art Unit 2655

Applicant(s): Mamoru KUDO et al.

Invention: PHASE-LOCKED LOOP CIRCUIT FOR REPRODUCING A CHANNEL CLOCK

**TO THE COMMISSIONER FOR PATENTS**

**RECEIVED**

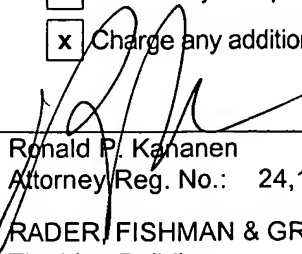
Transmitted herewith is an amendment in the above-identified application.

**NOV 14 2003**

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED						Technology Center
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate		
Total Claims	5	- 20 =	0	x	18.00	0.00
Independent Claims	1	- 3 =	0	x	86.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>						
Other fee (please specify):						
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:						240.00

- ☒ Large Entity ☐ Small Entity
- ☒ No additional fee is required for this amendment.
- ☐ Please charge Deposit Account No. 18-0013 in the amount of \$                     .  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$                      to cover the filing fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 18-0013 as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

  
Ronald P. Kananen  
Attorney Reg. No.: 24,104

Dated: November 7, 2003

RADER, FISHMAN & GRAUER PLLC  
The Lion Building  
1233 20th Street, N.W., Suite 501  
Washington, DC 20036  
(202) 955-3750  
DC139013

Docket No.: SON-22741  
(80001-2274)

NOV 07 2003



Application No.: 10/000,347

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Patent Application of

Mamoru KUDO et al.

Confirmation No. 9724

Serial No.: 10/000,347

Art Unit: 2655

Filed: December 4, 2001

Examiner: Gautam Patel

For: PHASE-LOCKED LOOP CIRCUIT FOR  
REPRODUCING A CHANNEL CLOCK

**RECEIVED**

NOV 14 2003

Technology Center 2600

RESPONSE UNDER 37 C.F.R. §1.111

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action mailed on  
September 15, 2003, please amend the above-identified application  
as follows:

IN THE TITLE

Please replace the title with the following title.

-- PHASE-LOCKED LOOP CIRCUIT FOR REPRODUCING A CHANNEL CLOCK

--